

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10825694
	Filing Date		2004-04-16
	First Named Inventor	John Harper	
	Art Unit	2173	
	Examiner Name		
	Attorney Docket Number	119-0041US	

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	1	5388201		1995-02-07	Hourvitz et al.	
	2	5490246		1996-02-06	Brotsky et al.	
	3	5651107		1997-07-22	Frank et al.	
	4	5764229		1998-06-09	Bennett	
	5	5793376		1998-08-11	Tanaka et al.	
	6	5854637		1998-12-29	Sturges	
	7	5872729		1999-02-16	Deolalikar	
	8	5877741		1999-03-02	Chee et al.	

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9	5877762		1999-03-02	Young et al.	
10	5933148		1999-08-03	Oka et al.	
11	5949409		1999-09-07	Tanaka et al.	
12	6006231		1999-12-21	Popa	
13	6031937		2000-02-29	Graffagnino	
14	6075543		2000-06-13	Akeley	
15	6166748		2000-12-26	Van Hook et al.	
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21	6272558		2001-08-07	Hui et al.	
22	6321314		2001-11-20	Van Dyke	
23	6362822		2002-03-26	Randel	
24	6369823		2002-04-09	Ohba	
25	6369830		2002-04-09	Brunner et al.	
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27	6411301		2002-06-25	Parikh et al.	
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31	6452600	2002-09-17	Parikh et al.
32	6456290	2002-09-24	Parikh et al.
33	6457034	2002-09-24	Morein
34	6466218	2002-10-15	Parikh et al.
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36	6525725	2003-02-25	Deering
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38	6542160	2003-04-01	Abgrall
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54	6717599		2004-04-06	Olano	
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56	6801202		2004-10-05	Nelson et al.	
57	6867779		2005-03-15	Doyle	
58	6906720		2005-06-14	Emberling et al.	
59	6911984		2005-06-28	Sabella	
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	2	20020067418		2002-06-06	Hiroaki	
	3	20020093516		2002-07-18	Brunner et al.	
	4	20020118217		2002-08-29	Fujiki	
	5	20020171682		2002-11-21	Frank et al.	
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	7	20030123739		2002-09-05	Graffagnino	
	8	20030174136		2003-09-18	Emberling et al.	
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	1	0548586	EP		1993-06-30	International Business Machines Corporation		<input type="checkbox"/>
	2	0694879	EP		1996-01-31	Canon Kabushiki Kaisha		<input type="checkbox"/>
	3	0972273	EP		2004-03-24	Intergraph Hardware Technologies Company		<input type="checkbox"/>
	4	1383080	EP		2004-01-21	Microsoft Corporation		<input type="checkbox"/>

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5	98/45815	WO		1998-10-15	Intergraph Corporation	<input type="checkbox"/>
6	02/09039	WO		2002-01-31	ViewPoint Corporation	<input type="checkbox"/>
7	04/027707	WO		2004-04-01	DaimlerChrysler AG	<input type="checkbox"/>

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	1	European Search Report received in corresponding application number EP 06 02 6984 dated May 8, 2007	<input type="checkbox"/>
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	6	Written Opinion of the International Searching Authority received in corresponding application number PCT/US2005/008804 dated July 27, 2005 (119-0033WO)	<input type="checkbox"/>

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7	International Search Report received in corresponding application number PCT/US05/008804 (119-0033WO) dated July 27, 2005	<input type="checkbox"/>
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9	Haeberli et al.; "The Accumulation Buffer: Hardware Support for High-Quality Rendering;" Computer Graphics, New York, NY; vol. 24, August 1, 1990; pgs. 309-318	<input type="checkbox"/>
10	nVIDIA; "Cg - Teaching Cg;" Power Point Presentation , Author and date unknown	<input type="checkbox"/>
11	Shantzis; " A Model for Efficient and Flexible Image Computing;" Computer Graphics Proceedings, Annual Conference Series; 1994; pp. 147-154	<input type="checkbox"/>
12	Akeley et al; "Real-Time Graphics Architecture;" located at http://www.graphics.stanford.edu/courses/cs448a-01-fall/ ; The OpenGL® Graphics System - CS448 Lecture 15, Fall 2001; 99. 1-20	<input type="checkbox"/>
13	Gelder, et al.; "Direct Volume Rendering with Shading via Three-Dimensional Textures;" Computer Science Dept.; University of California, Santa Cruz, CA 95064	<input type="checkbox"/>
14	Elliot; "Programming Graphics Processors Functionality"	<input type="checkbox"/>
15	Segal et al; "The OpenGL® Graphics System: A Specification (Version 1.5)" Copyright © 1992-2003 Silicon Graphics, Inc.; October 30, 2003	<input type="checkbox"/>
16	"GNU C Compiler Internal;" Internet article located at: http://en.wikibooks.org/wiki/GNU_C_Compiler_Internals/GNU_C_Compiler_Architecture_3_4	<input type="checkbox"/>
17	"Novillo; ""From Source to Binary: The Inner Workings of GCC;"" Internet article located at: http://www.rechat.com/magazine/002dec04/features/gcc/ ; Red Hat Magazine, Issue 2, December 2004"	<input type="checkbox"/>

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18	"Berlin et al.; ""High-Level Loop Optimizations for GCC;"" Internet article located at: http://gcc-ca.internet.bs/summit/2004/High%20Level%20Loop%20Optimizations.pdf ; GCC Developers' Summit, June 2-4, 2004, Ottawa, Canada"	<input type="checkbox"/>
19	"Novillo; ""OpenMP and automatic parallelization in GCC;""Internet article located at: http://people.redhat.com/dnovillo/Papers/gcc2006.pdf ;GCC Developers' Summit, June 28-30, 2006, Ottawa, Canada"	<input type="checkbox"/>
20	"Bothner; ""Gcc Compile Server;"" Internet article located at: http://gcc-ca.internet.bs/summit/2003/GCC%20Compile%20Server.pdf ;GCC Developers' Summit, May 25-27, 2003, Ottawa, Canada"	<input type="checkbox"/>

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